



The Test Connection, Inc.

GETTING TO THE POINT

CORELIS—Boundary-Scan Support

The Test Connection is happy to report that we are now a “Value Added Partner” (VAP) for Corelis, Inc. The Test Connection will be providing programming and other products, enabling us to support customers with quality boundary-scan test services for PC boards. In the 1st quarter of 2002 TTC signed an agreement with Corelis purchasing a complete system. TTC is now supporting the Corelis Scan-Plus as a stand-alone boundary-scan tester in conjunction with our Teradyne/GenRad 228X and Agilent Technologies 3070 test systems.

The Test Connection has concluded training for several test engineers and is now supplying programs for OEMs to be deployed at their contract manufacturer’s sites. With our vast experience in performing boundary-scan development on the GenRad and Agilent systems, plus providing testability reviews on new products, we feel that Corelis Scan-Plus is a significant additional feature to continually support in-circuit and functional testers. With increasing board densities and heavy use of BGA ICs, this new tool meets well with the demands of our current customer base. This adds capabilities to our testability reviews, using



ScanPlusDFT Analyzer to determine which board modifications can most improve testability on a limited access board. TTC now has a new method of Flash programming to offer customers: Corelis Scan-Plus Flash Programming. In-System-Programming (ISP) of all boundary-scan PLDs can now be accomplished on the bench top or as part of an in-circuit test program utilizing Corelis ScanPlus Runner™.

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Left to right: Ernie Sundblom - Senior Test Engineer in the New Hampshire office, Bill Horner - President, John Hanusick - New England Sales, Bert Horner - National Sales, Alan Niesel - New England Sales.

New Faces From New Places

TTC is proud to announce three new faces joining our team concentrating on the New England area. John Hanusick will be covering sales in the New York, Connecticut and northern New Jersey areas; Alan Niesel of Emerging Technology Associates will be representing us in the Maine, New Hampshire, Vermont, Massachusetts and Rhode Island areas; and Ernie Sundblom, is a new Test Engineer in our Manchester, NH office.

John Hanusick comes to us with thirty years experience in Electronic Engineering management at Northrop Grumman in Connecticut. He also has a background in ISO certification, auditing and accreditation. John completed his B.S. in Electrical Engineering at Bridgeport Engineering Institute. We look forward to gaining knowledge from his experience and expertise and selling presence in the southern portion of New England.

Alan Niesel represents TTC through Emerging Technology Associates. Combined he has over ten years experience as regional sales manager for Synergetrix and TTI Testron, and over fifteen years experience working with the In-Circuit test development and fixture fabrication industries. Alan completed his A.A. Degree in Electrical Engineering from San Jose State and will be a valuable asset to our New England sales team.

Ernie Sundblom brings twenty plus years experience as an ATE Test and Applications Engineer to our team. As a test engineer he has numerous years experience with GenRad in-circuit test development, device modeling, boundary-scan, panel, Flash and ISP tests. He also has over ten years experience as an Application Engineer and Technical Instructor. We welcome Ernie on board at our Manchester, NH office.

Folsom's Fast Facts

CAD or Gerber?

CAD data for a PCB is a database that contains all net, component, etch, drill, board(outline) and layer information including silkscreen, solder mask and power-plane data. Gerbers are pictures of each layer of the board. No components or component pins are defined in Gerbers. No nets are defined in Gerbers. The layer information is external to the Gerbers. If you stack up the Gerbers, you see a picture of the board, but all the links that are in the CAD database are missing. Gerbers can be used to fabricate the bare board and load the components, but not to develop a test for the board. TTC can process Gerbers, but it is expensive and time consuming and thus not appropriate for quoting purposes.



'Good CAD' is required for an accurate complete quote. TTC uses Router Solutions Inc. software to process CAD data. 'Good CAD' is, for most CAD systems, simply the ASCII output of the CAD database. But for a CAD system such as Cadence Allegro, which has no fixed ASCII output, an extract script must be run to export files that can be imported into RSI's CAMCAD Professional. Please contact us to let us help you to generate 'Good CAD'.



The Test Connection, Inc. would like to thank everyone who stopped by our booth at the Nepron East 2002 show. The show took place at the Bayside Exposition Center on June 11th and 12th.

Attendance at the show was respectable in regards to the current industry pace. In addition to some of The Test Connection staff, representatives from ECT, TTI Testron and Corelis, Inc. were available to answer any questions. We look forward to seeing everyone again next year.

Testability Reviews

Have a new design and need a testability review? Let The Test Connection provide a DFT. We have the tools for any CAD system, the staff and experience to complete the DFT. Get the Testability and Strategy in your design before you layout your new printed circuit boards. Save money and improve **Quality** in your new products. Make them testable.

The Test Connection is in their 23rd year of test program development and is the oldest test engineering house in the USA supporting Agilent 3070 and Teradyne/GenRad Test Station, 228X testers.

A Test Engineer on Your Floor

Having In-circuit problems? The Test Connection can provide a test engineer on your production floor for a few days or a few weeks. Call us today to discuss your specific needs for a test engineer.

